

1/25

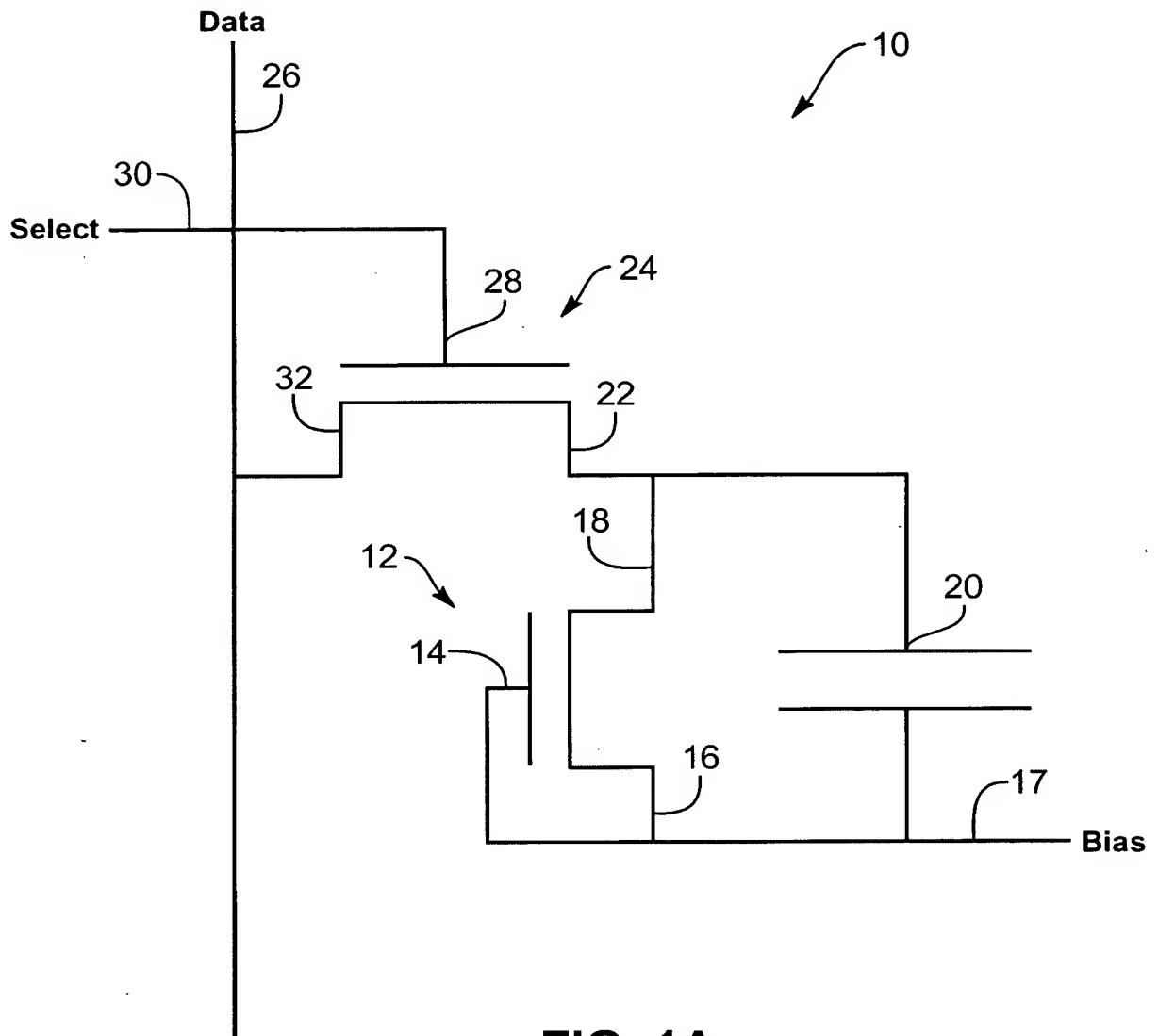


FIG. 1A

FIG. 1B

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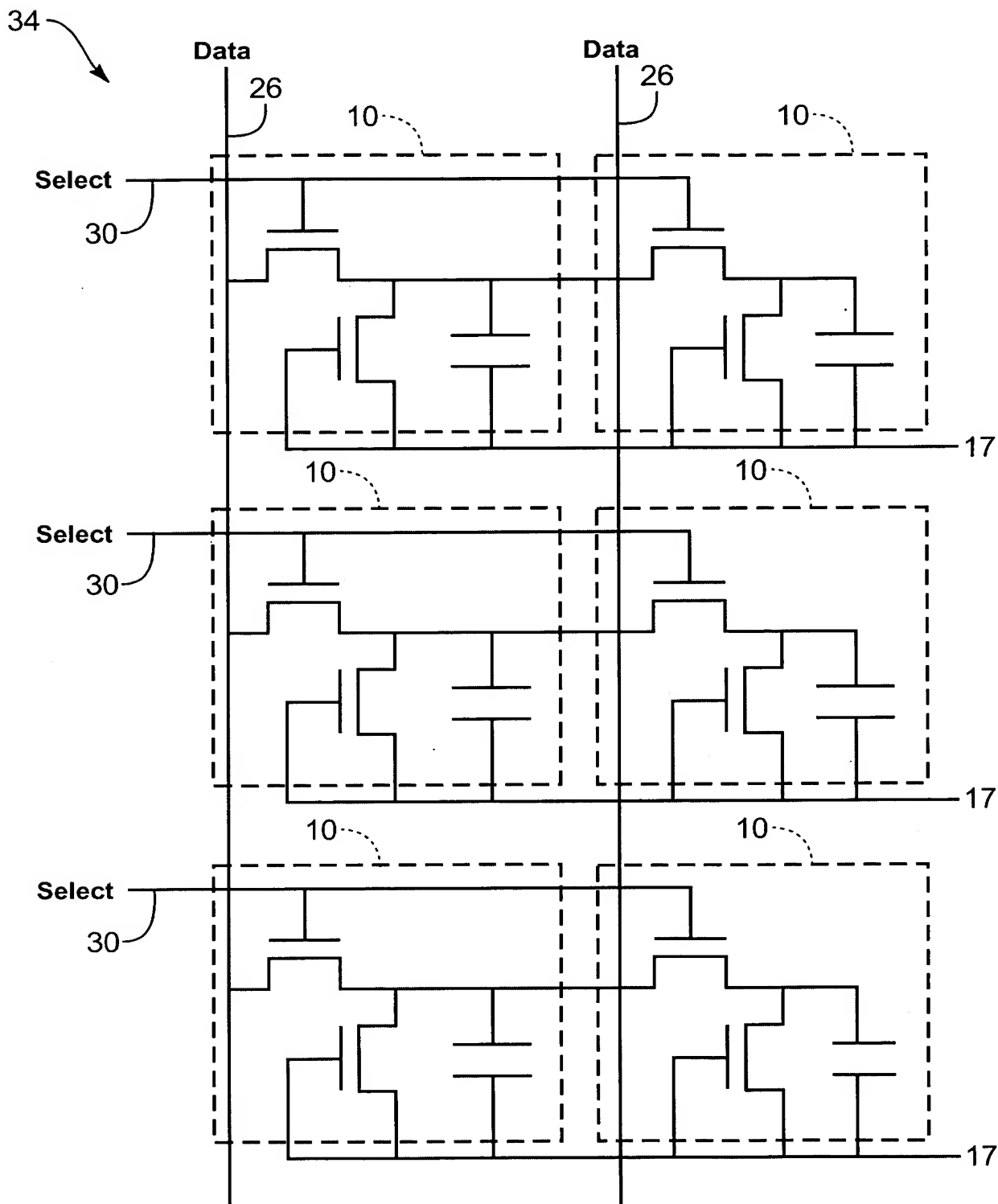


FIG. 2

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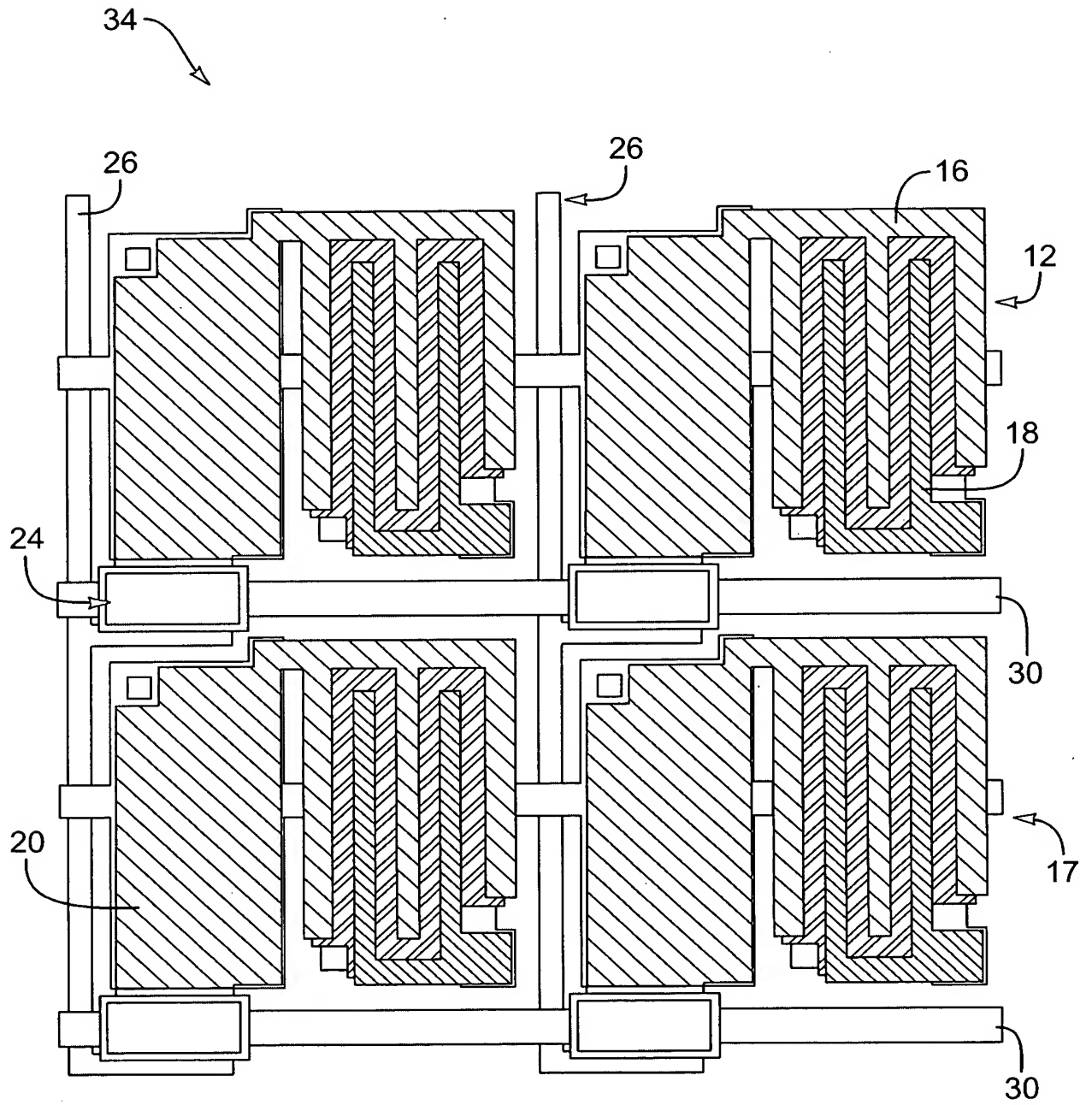


FIG. 3

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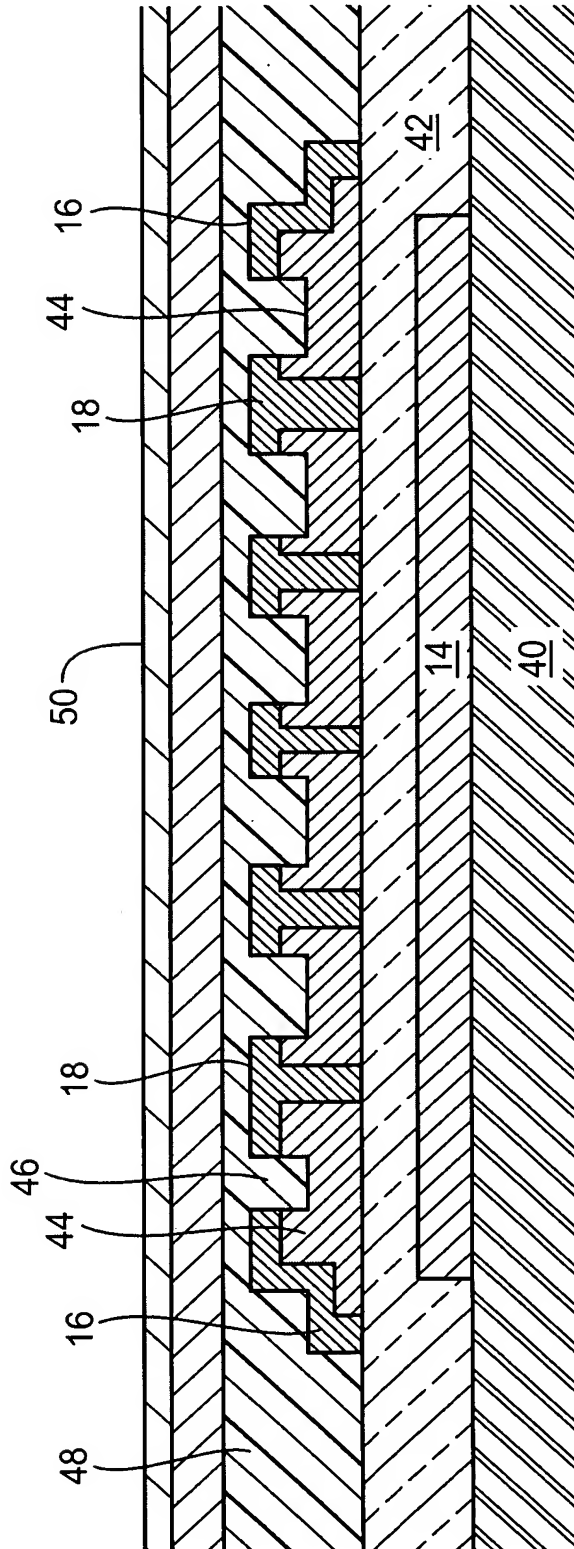


FIG. 4

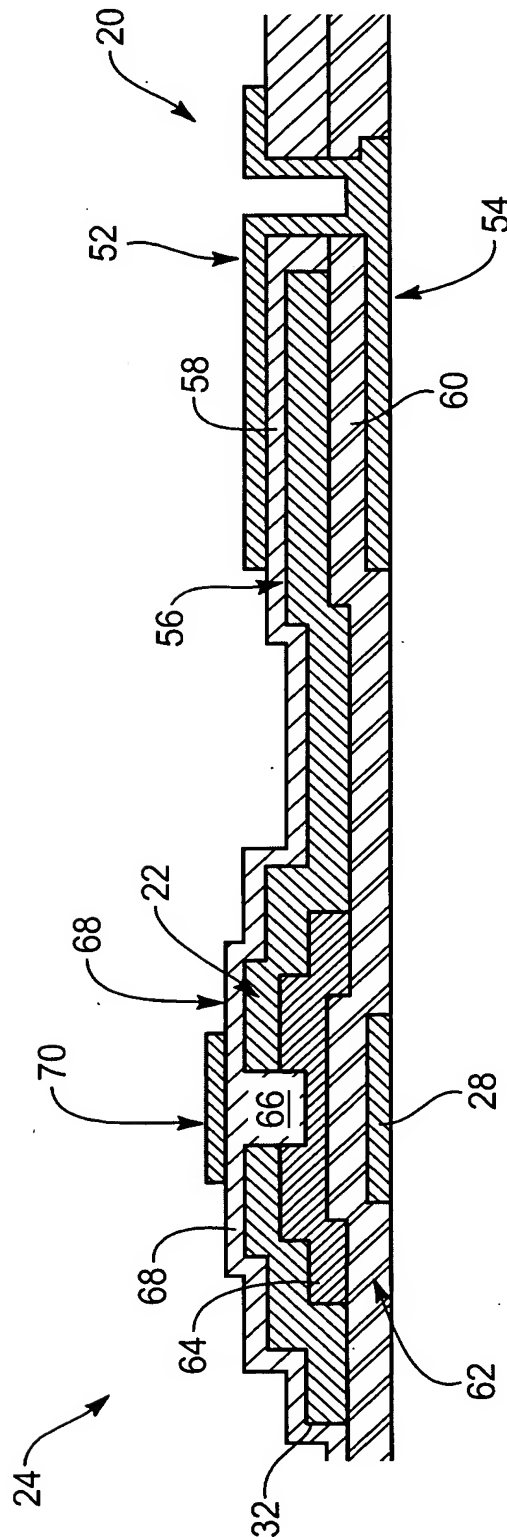


FIG. 5

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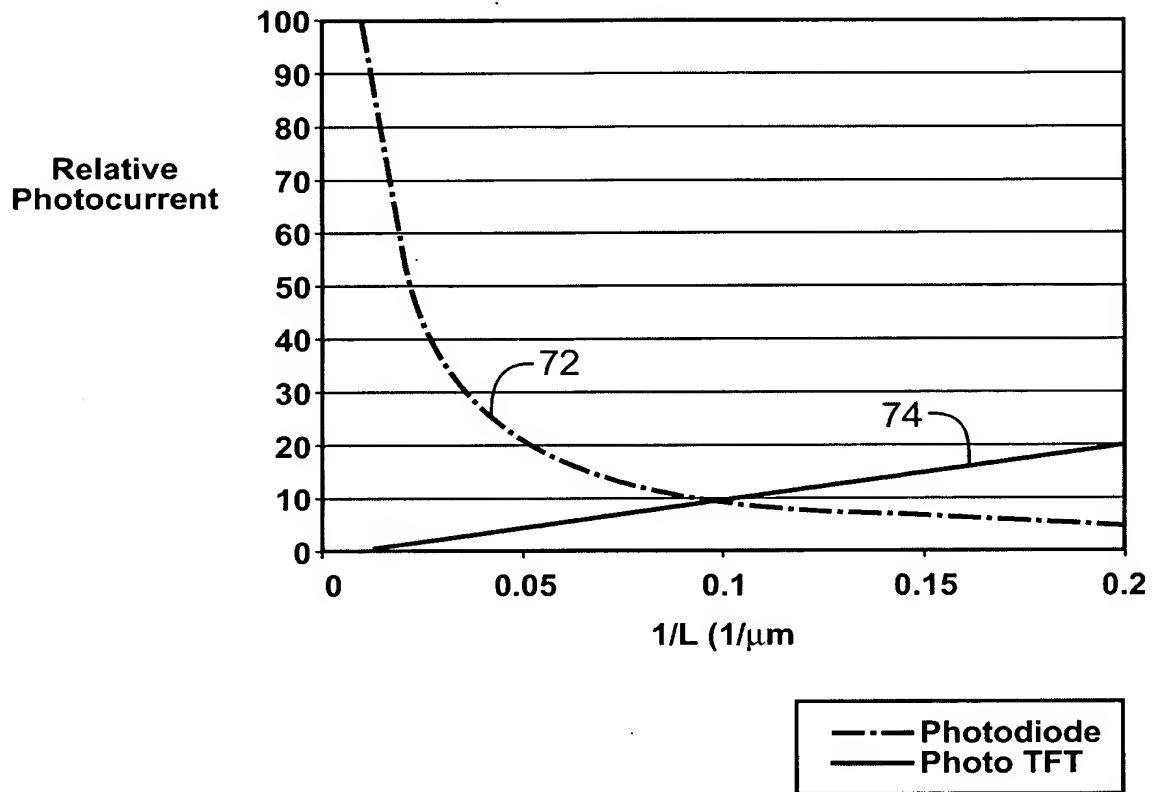


FIG. 6

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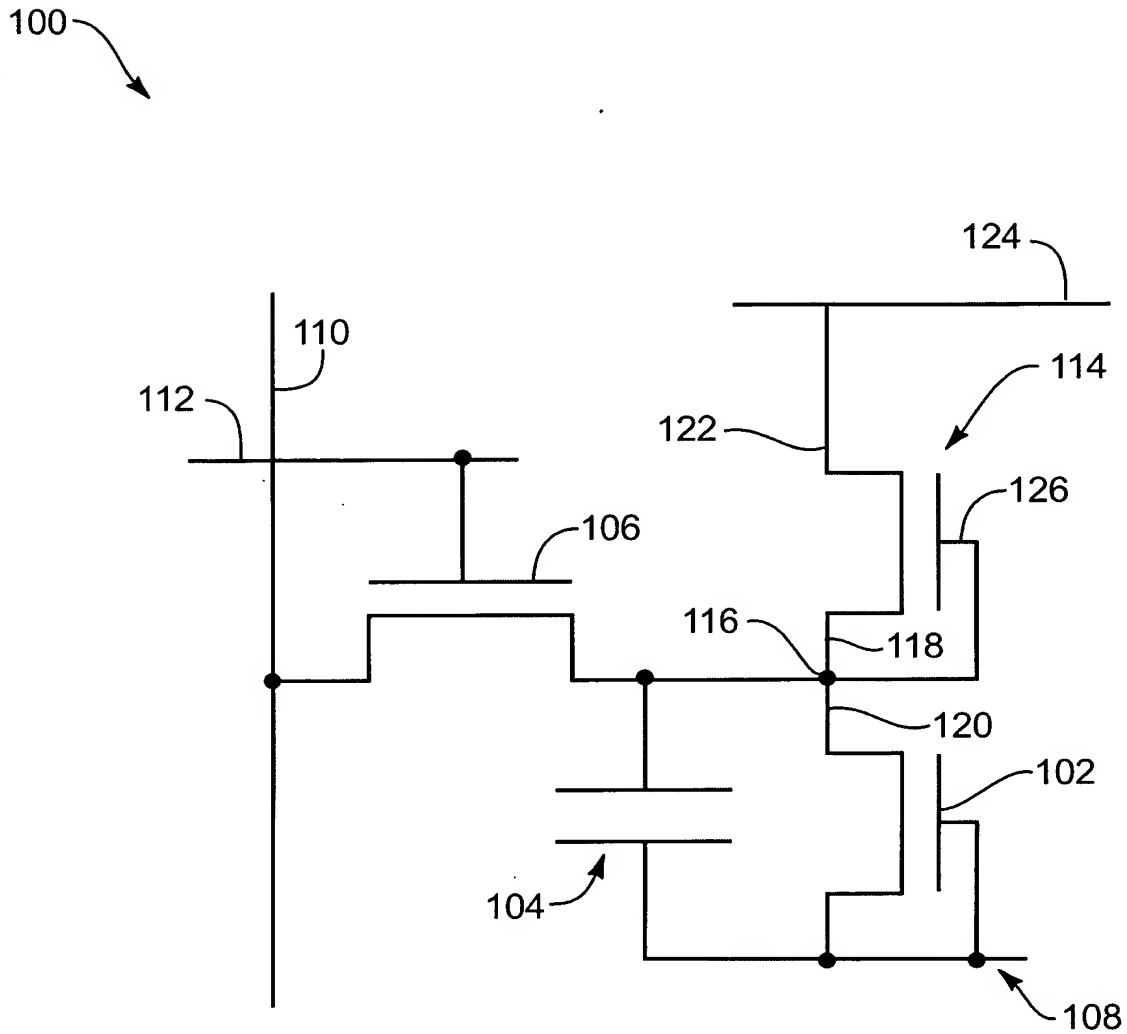


FIG. 7

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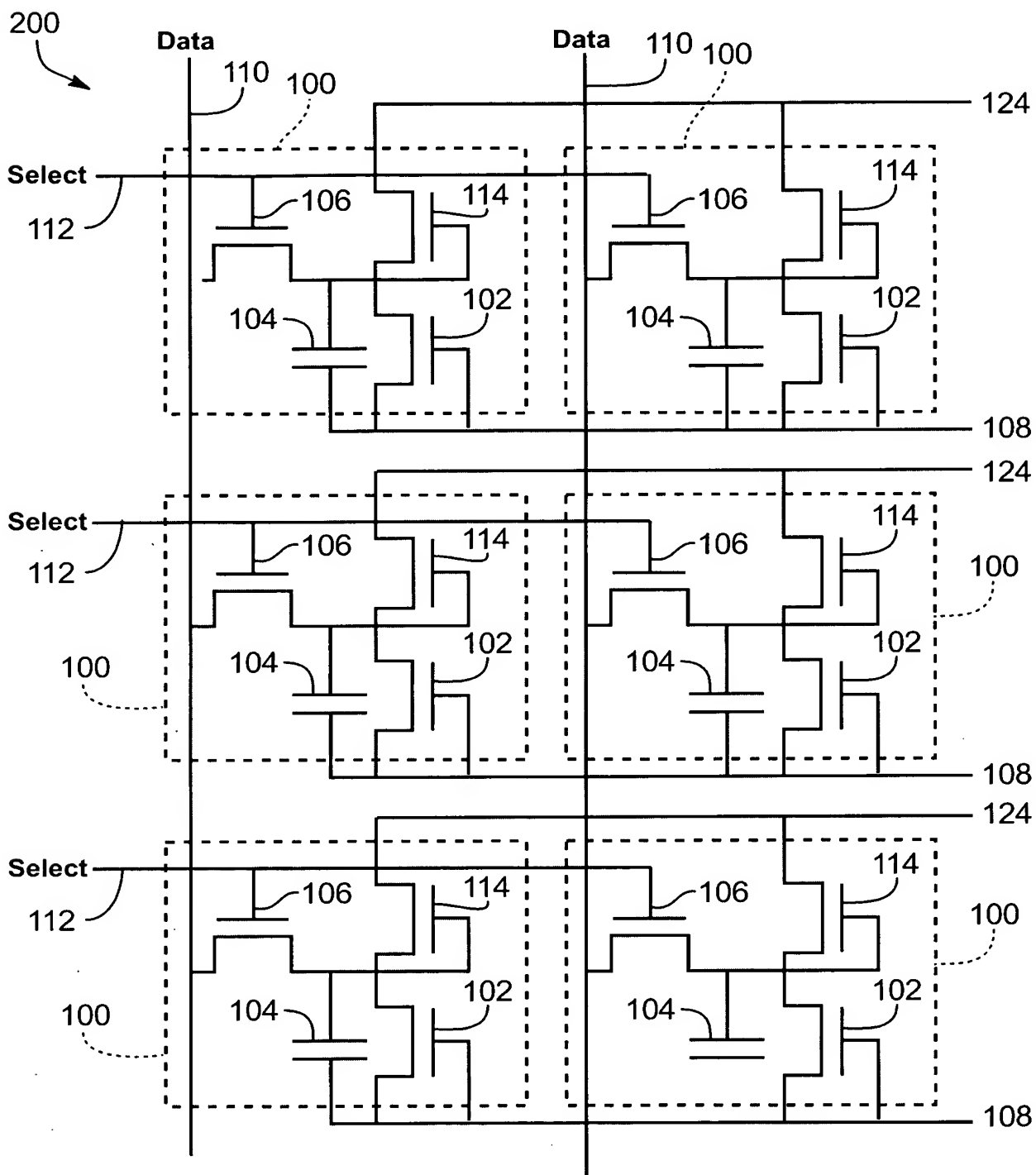


FIG. 8

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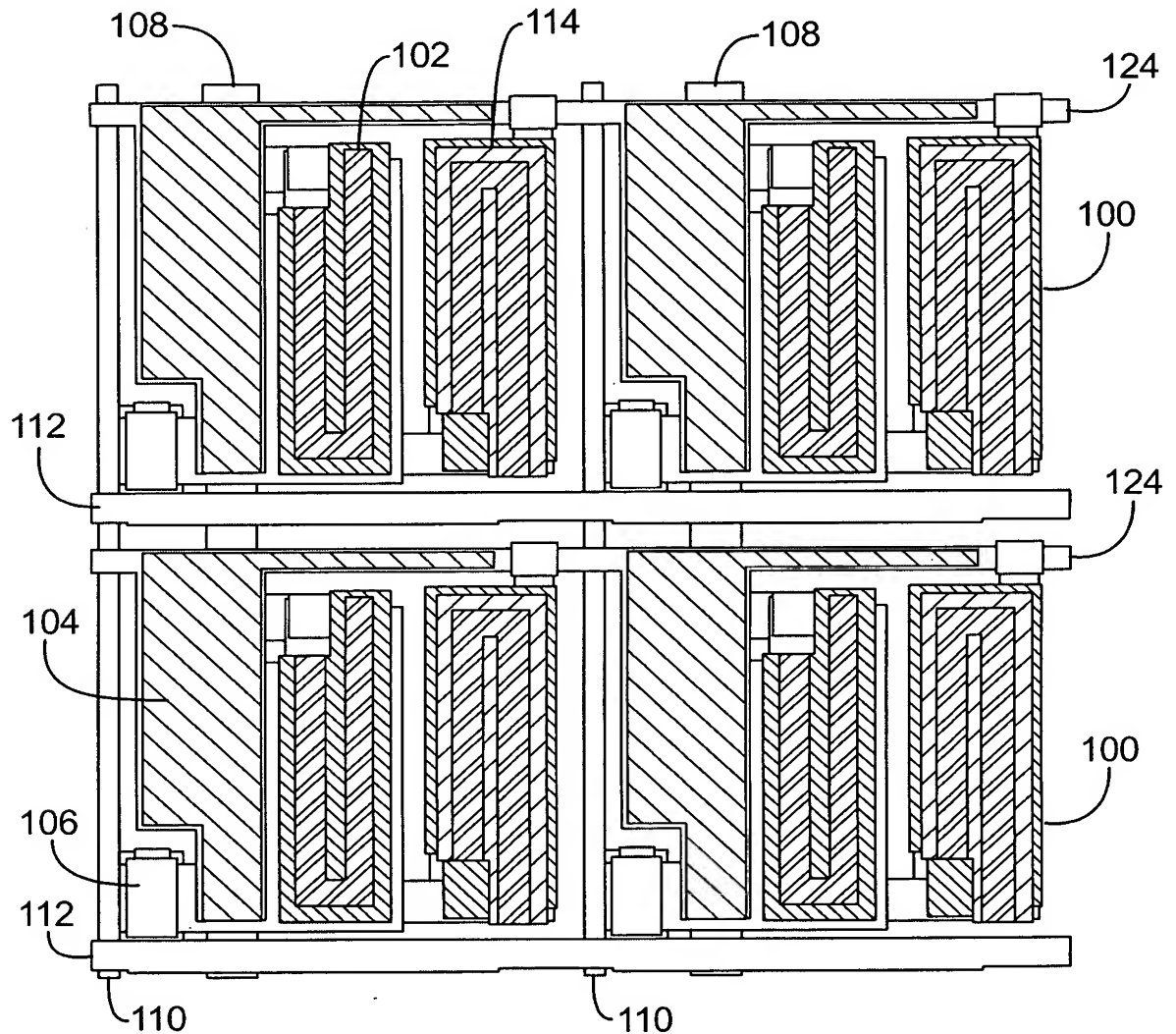


FIG. 9

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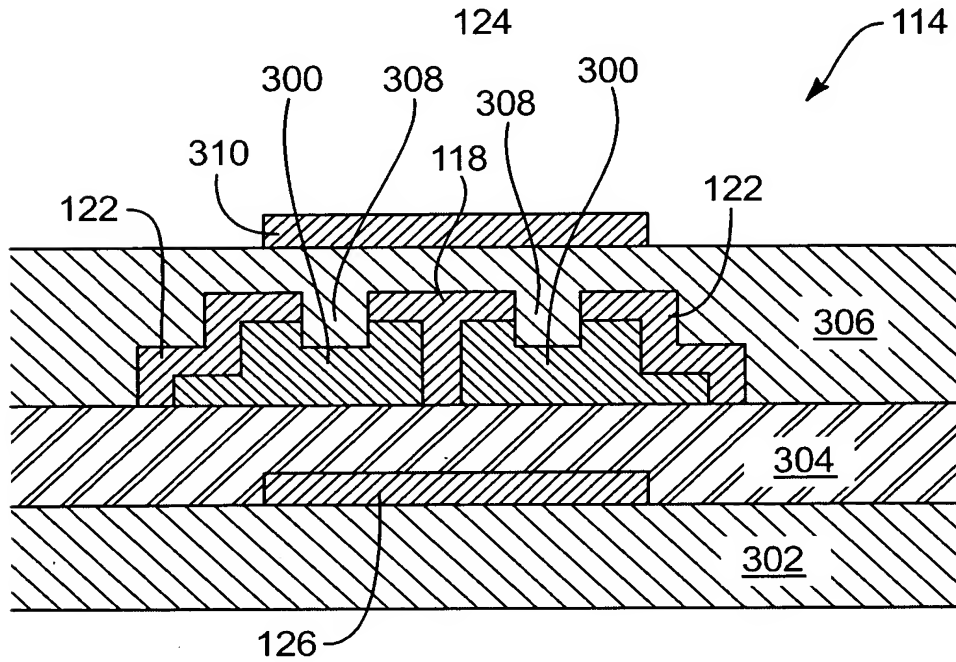


FIG. 10

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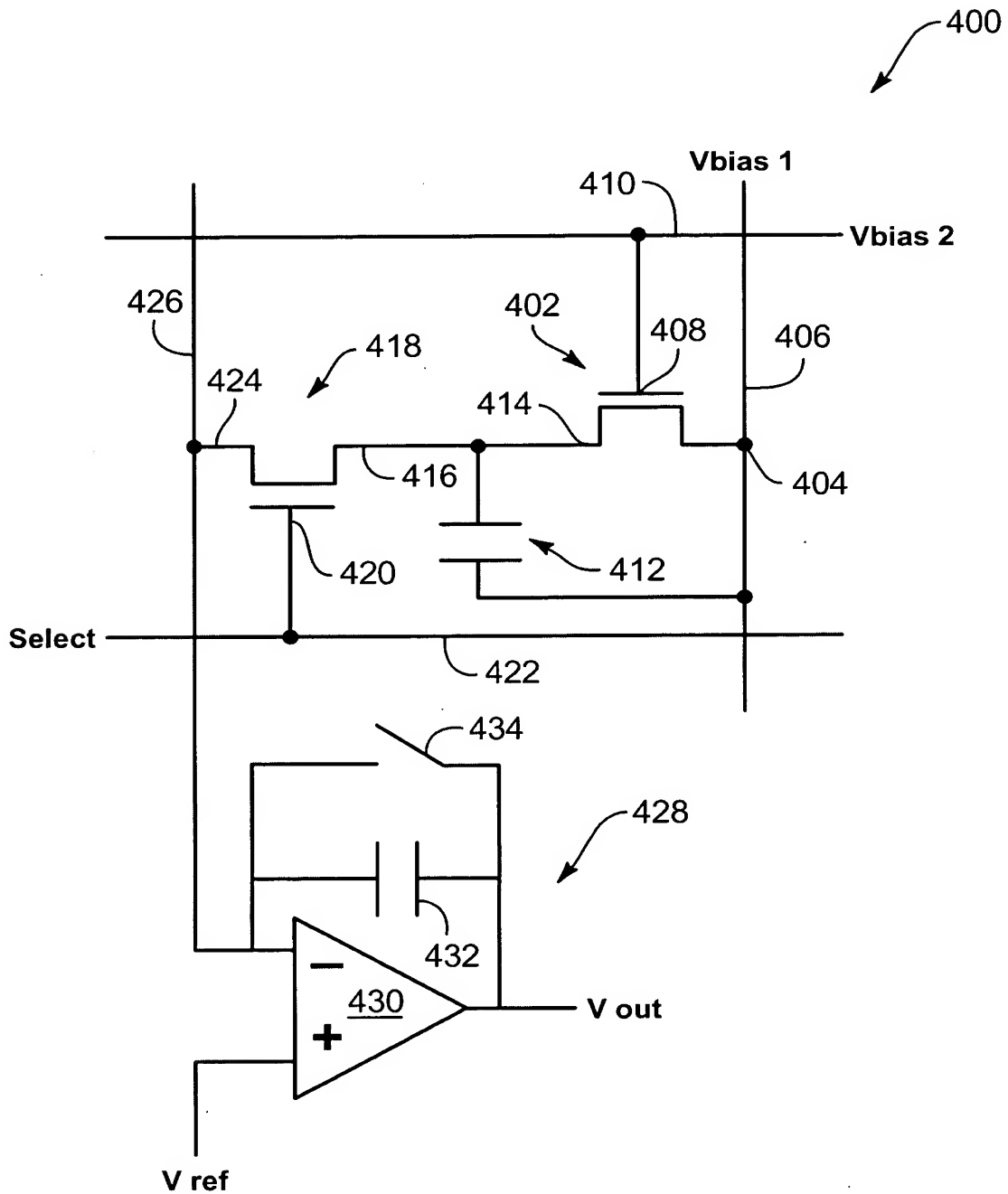


FIG. 11

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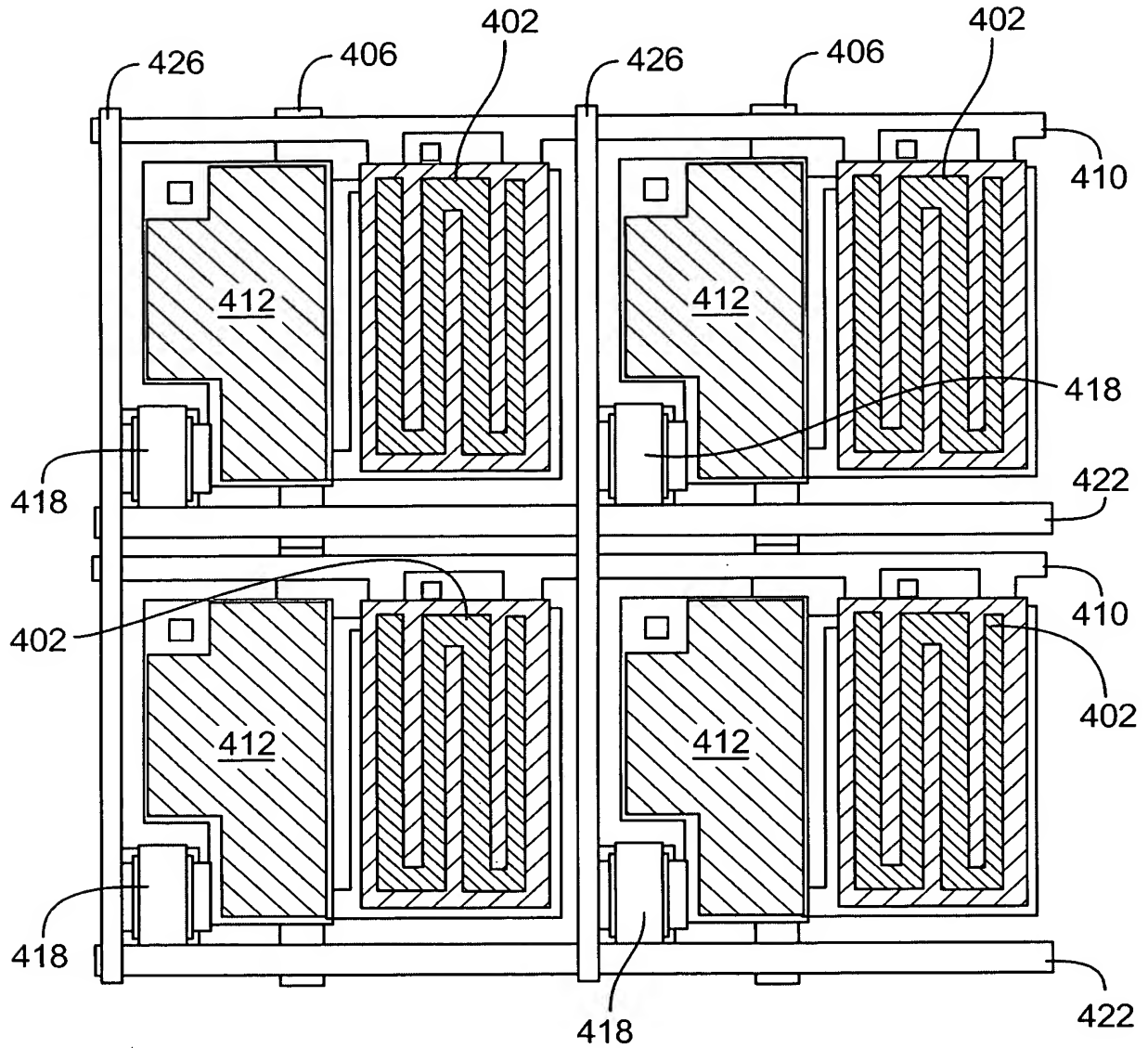
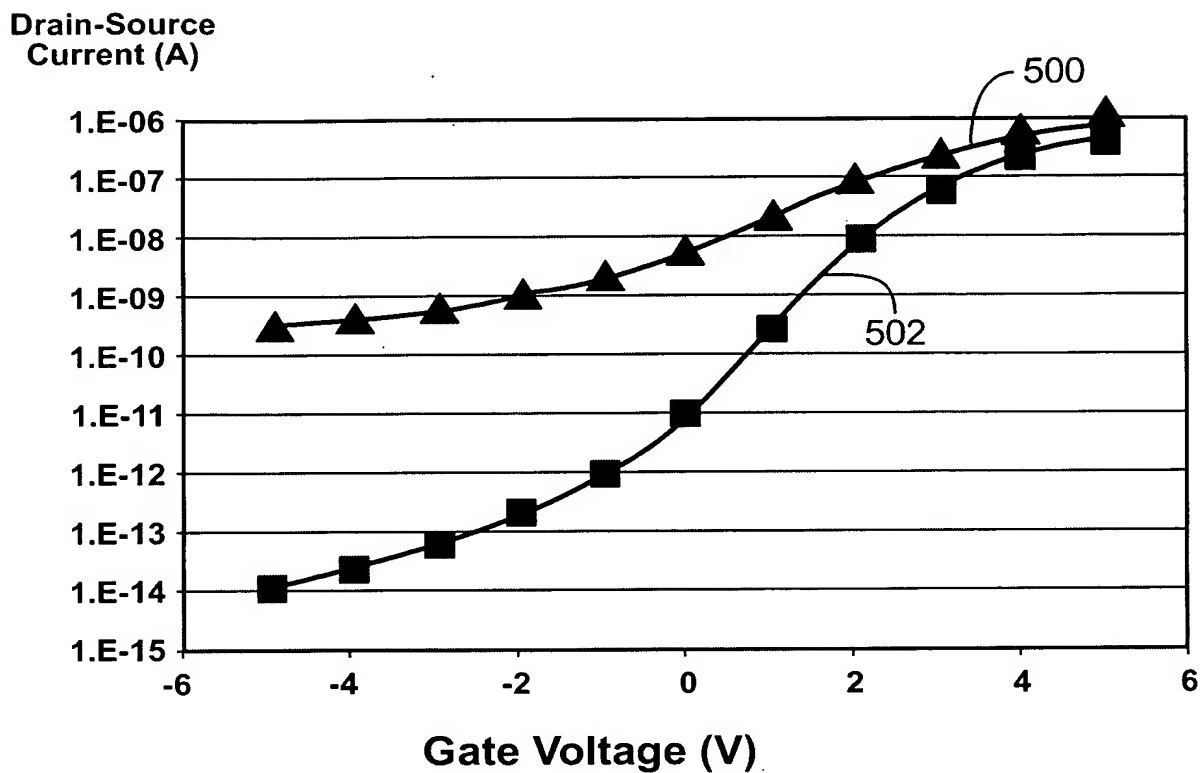


FIG. 12

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Dark Current and Photocurrent vs Gate Voltage (V_{bias}) at $V_{ds}=(V_{bias1})+5V$ for a-Si TFT with $W/L = 200 \mu m/6 \mu m$

FIG. 13

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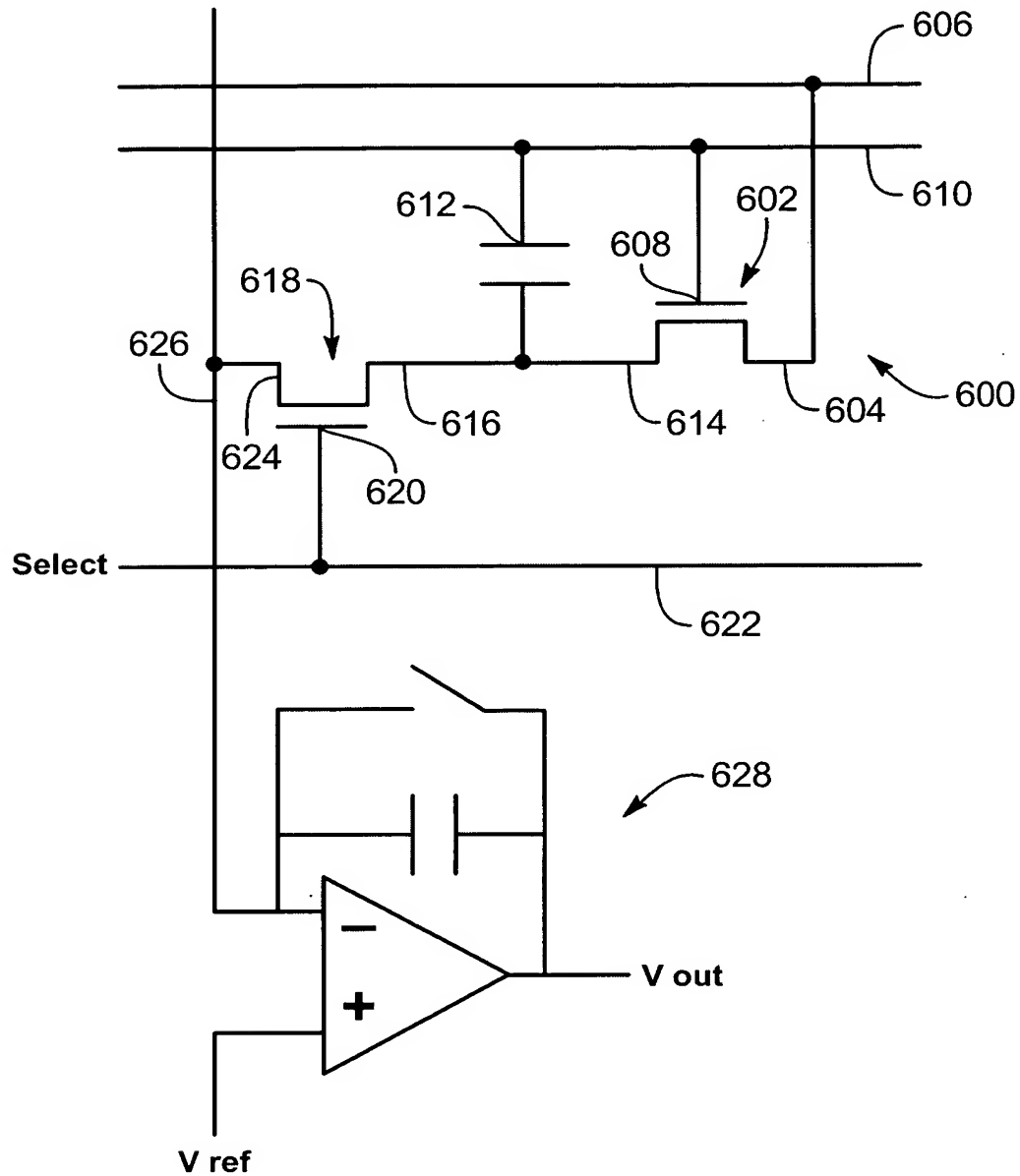


FIG. 14

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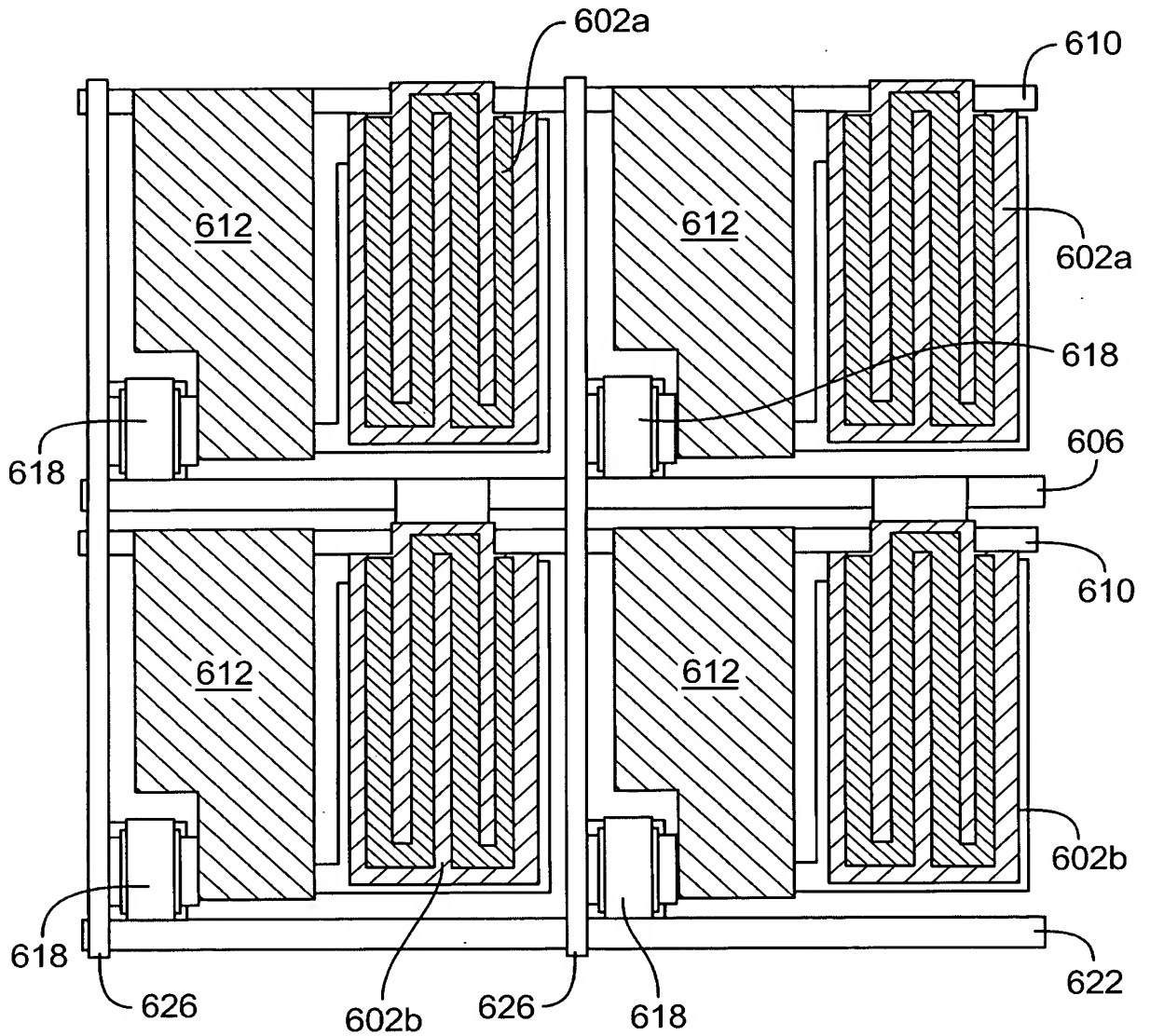


FIG. 15

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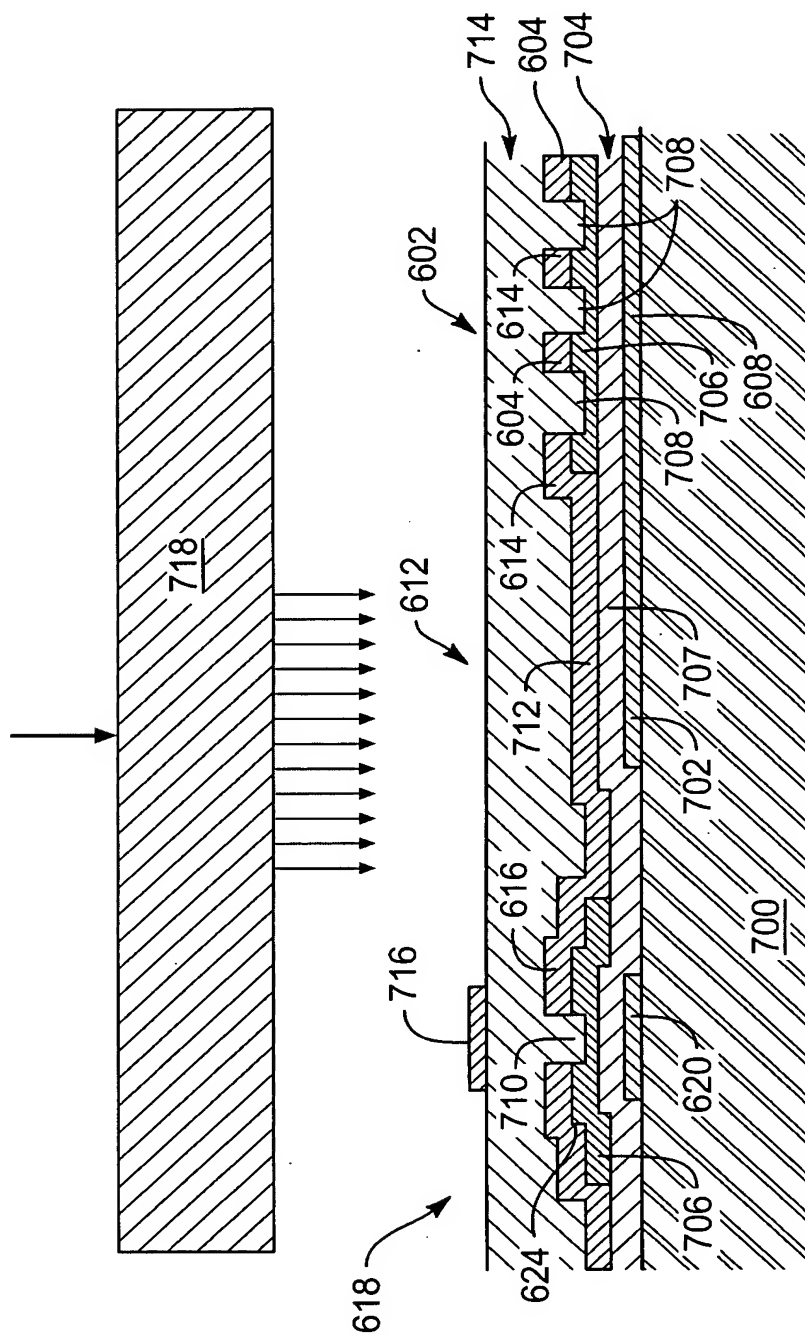


FIG. 16

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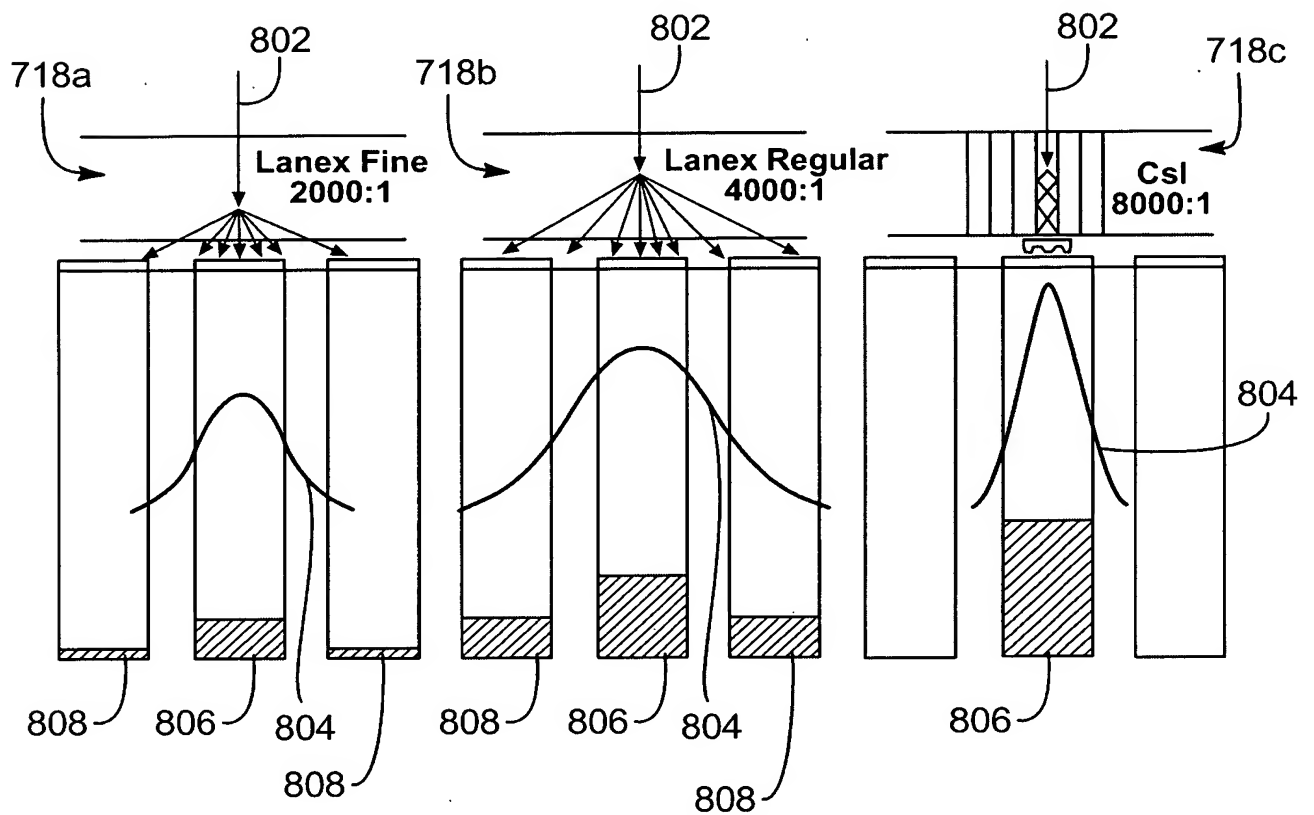


FIG. 17

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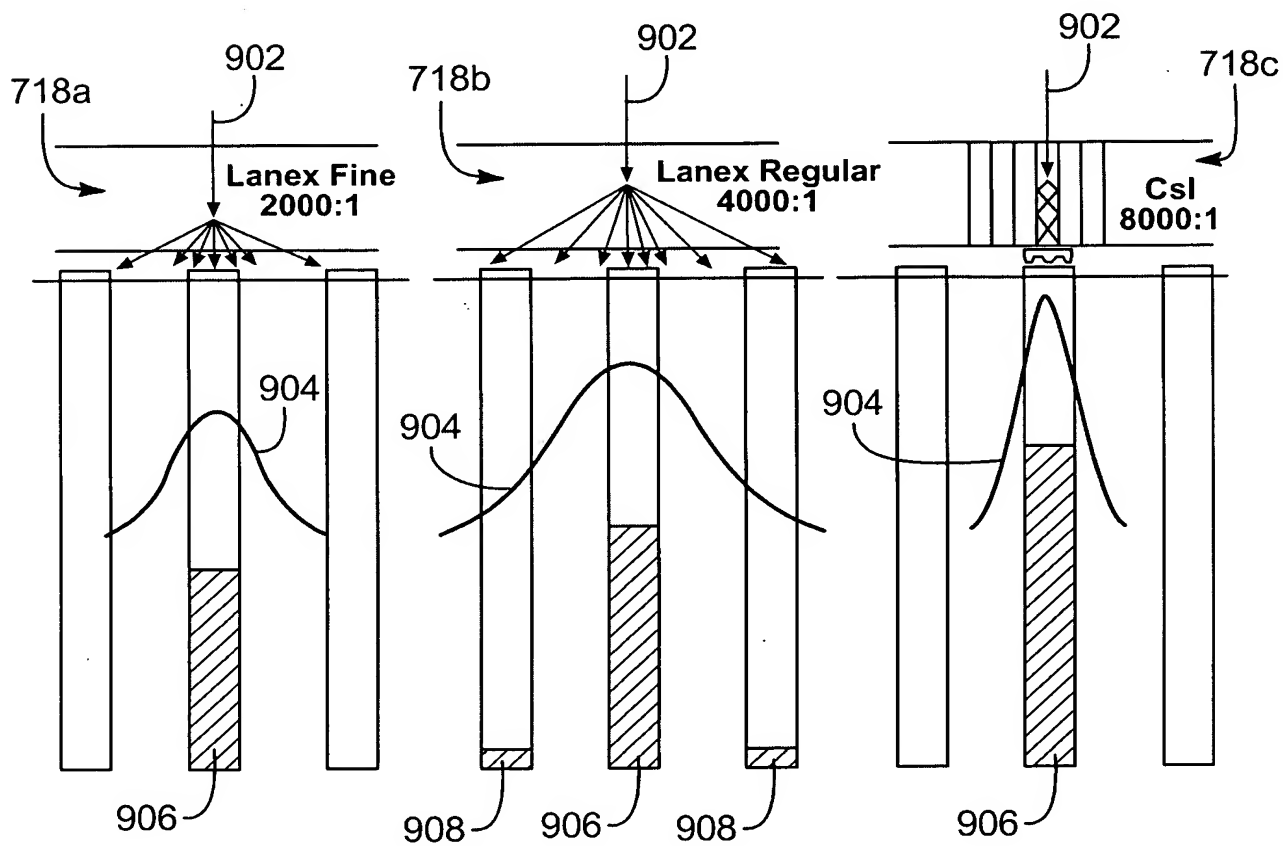


FIG. 18

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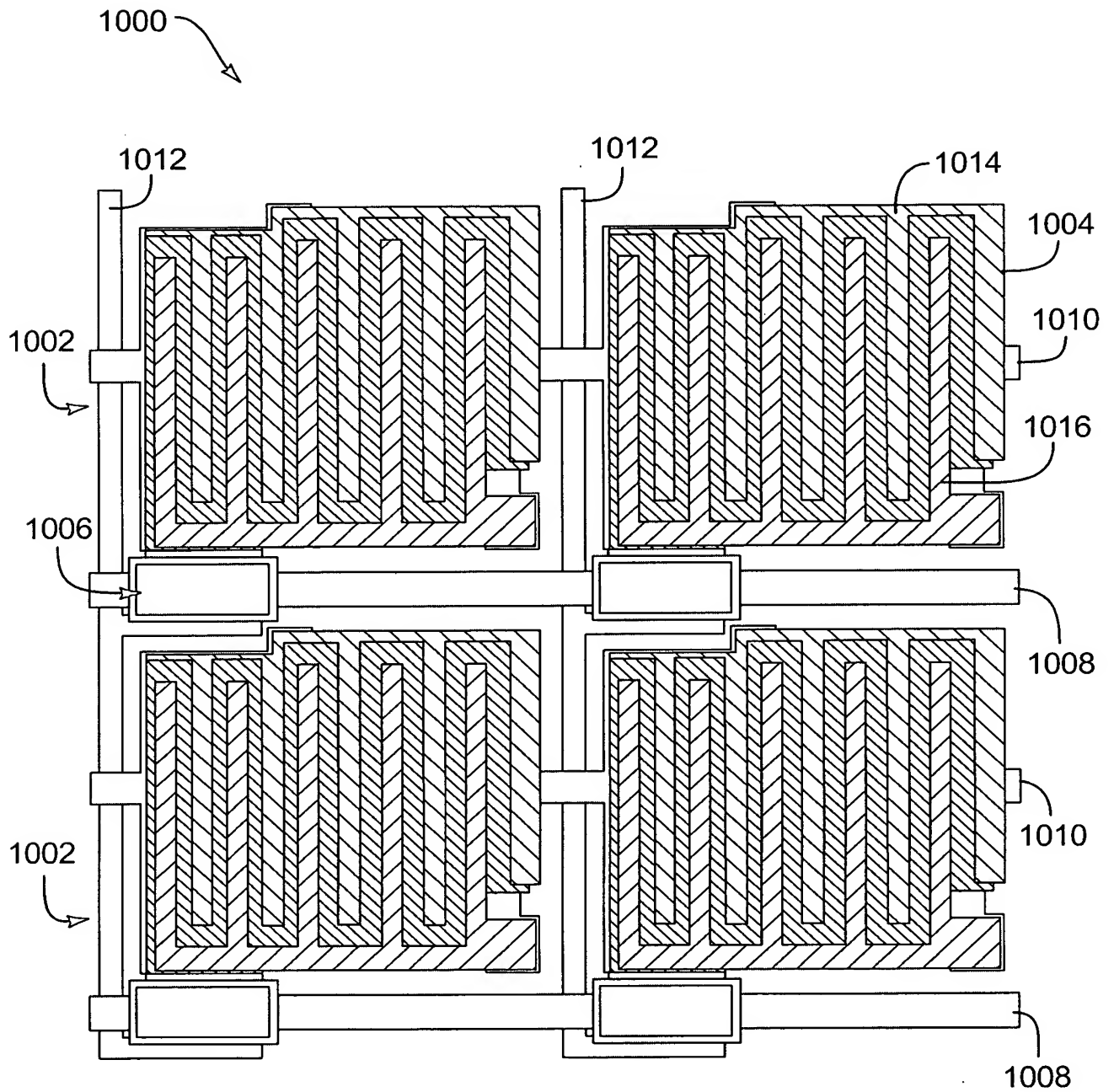


FIG. 19

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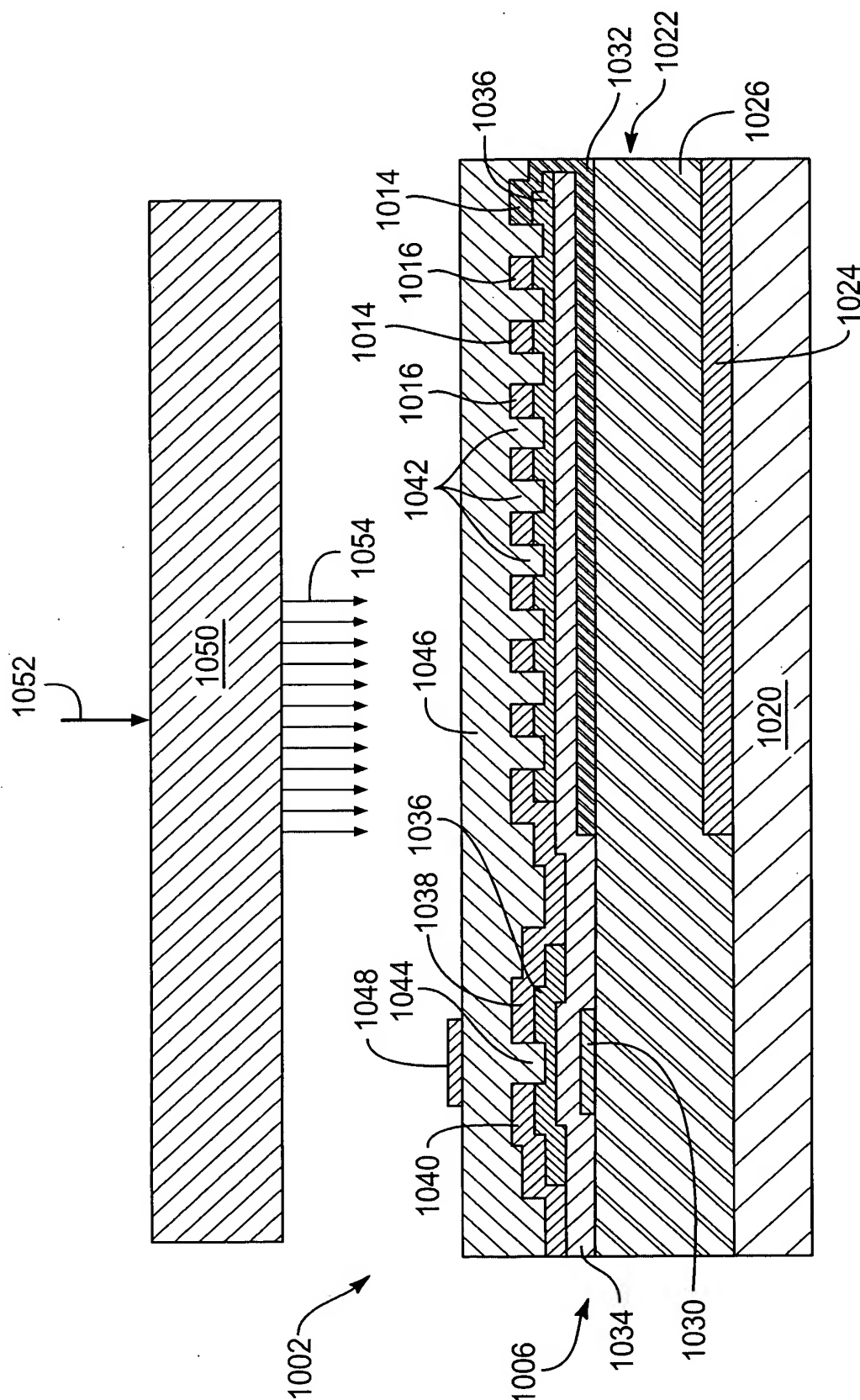


FIG. 20

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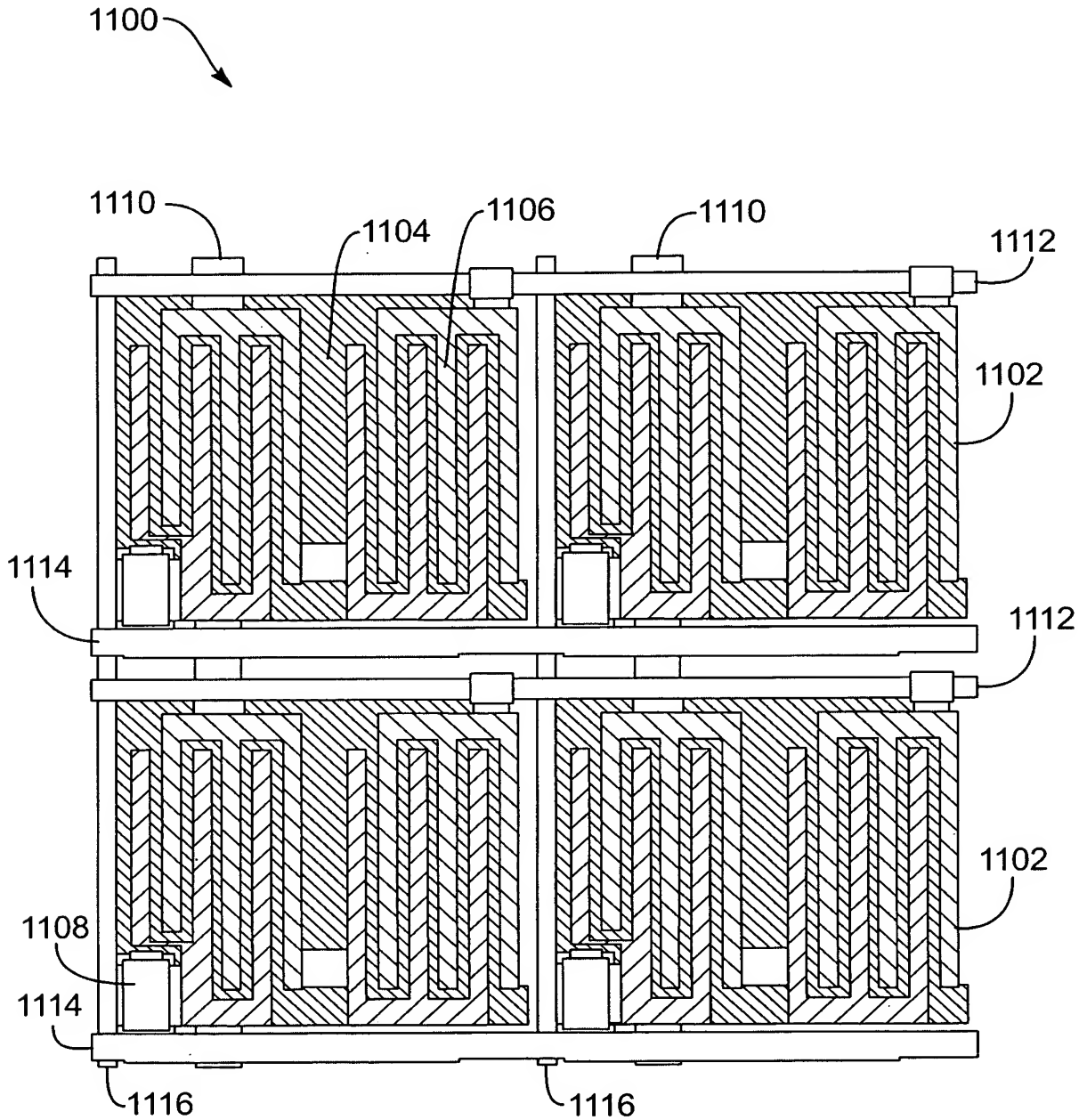


FIG. 21

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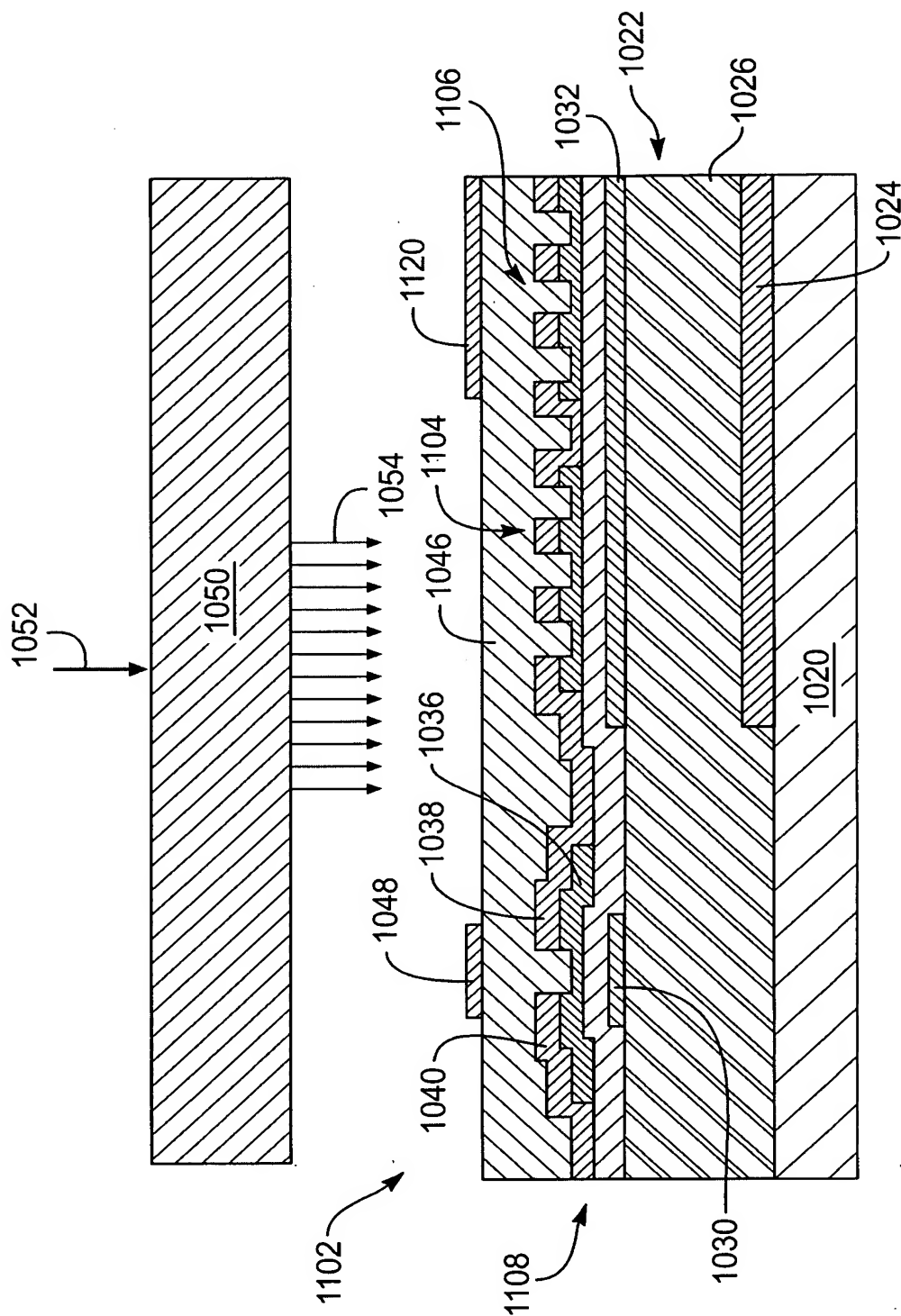


FIG. 22

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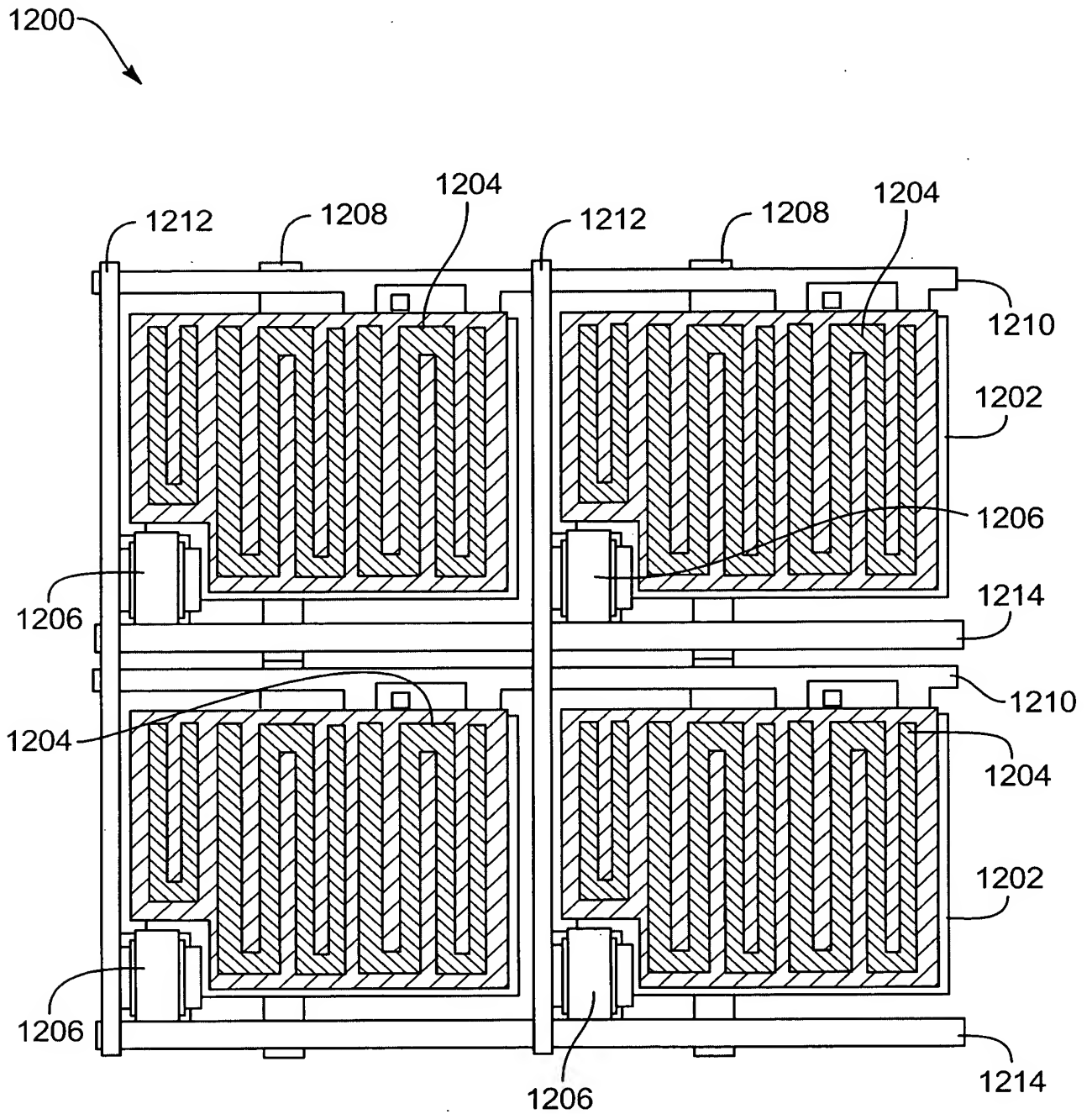


FIG. 23

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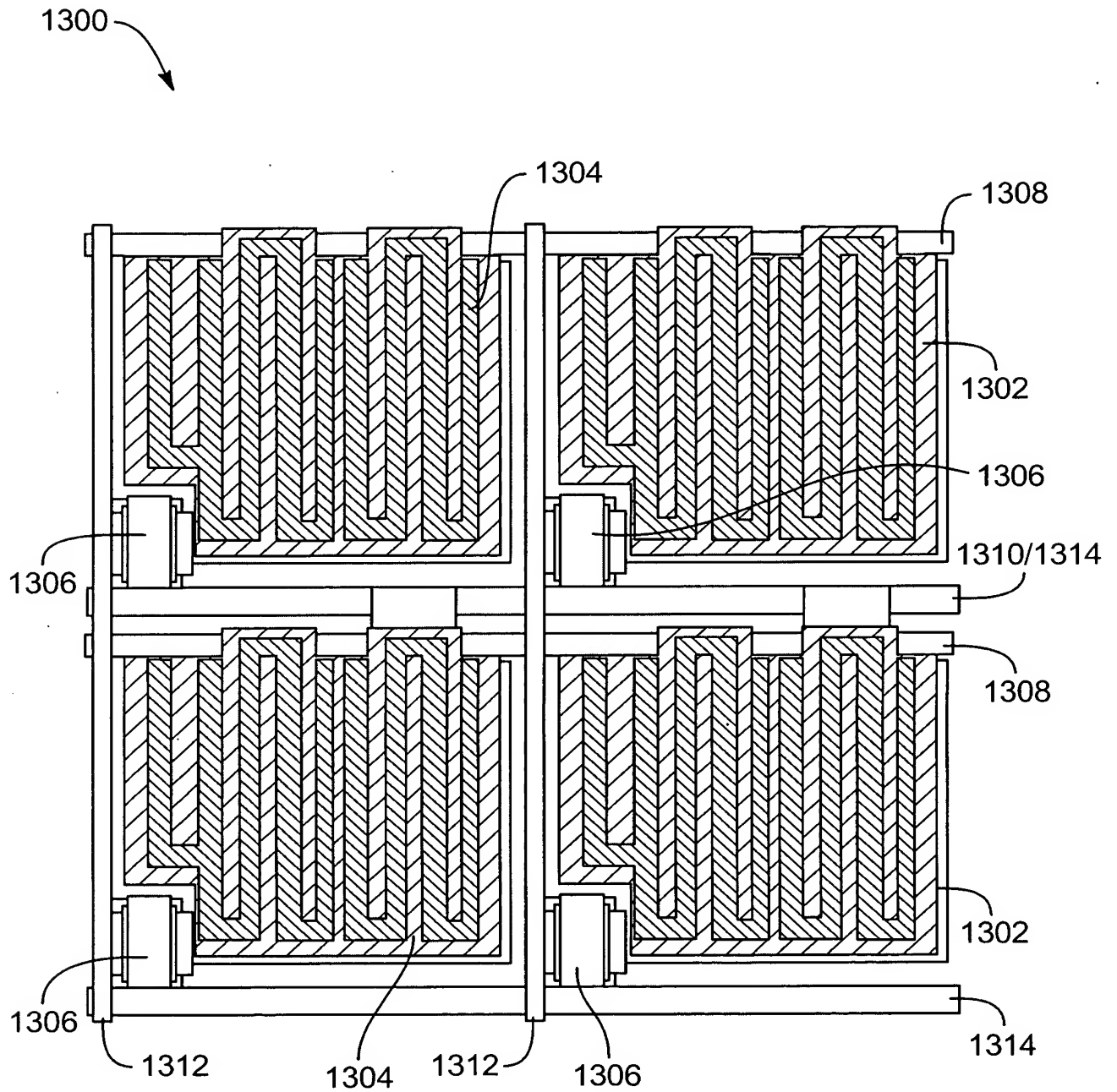


FIG. 24